



*The Institute of Electronics, Information, and Communication Engineers
Engineering Sciences Society Technical Committee on VLSI Design Technologies*

***Excellent Student Author Award
for ASP-DAC 2014***

Trung Anh Dinh

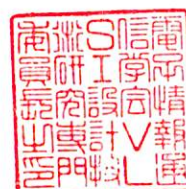
Ritsumeikan University, Japan

Paper Title:

*“A Network-Flow-Based Optimal Sample Preparation
Algorithm for Digital Microfluidic Biochips”*

Makoto Ikeda

池田 誠



Chair of

IEICE ESS Technical Committee on VLSI Design Technologies

March 3, 2014